

Extreme Scale Software Stack Program Summary of Impact on Science

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DEGAS Project: Impact of advances in data Structures and Runtime Support for Irregular Data-Intensive Applications

Speedups

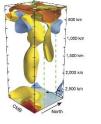
- Distributed hash table
 - Applications: HipMer (genomics)
- Irregular data exchange
 - Applications: AMR, HPGMG
- Irregular global matrix update
 - Applications: NWChem, seismic tomography
- Distributed work queue
 - Applications: NWChem, Hartree-Fock
- Dynamic task graph
 - Applications: Sparse symmetric matrix solver



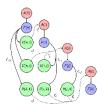




1.2x



6x

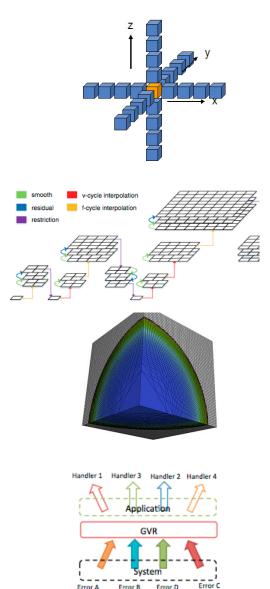


2x

1.2x

D-TEC Project: Impact of advances on DSL technologies, compilers and runtime systems

- AMR Shift Calculus DSL with ROSE/Polyopt
 - 7.9x for 3D 125 pts stencil
 - 7.3x for 2D 81 pts stencil
- Halide DSL
 - 4.25x for MiniGMG
 - 1.8x on GPU for HPGMG
- Bamboo
 - 1.27x in 32K size for algebraic multigrid
 - 1.29x with 96K grid cells for 3D stencil
- LULESH with X10
 - 1.12x better performance
 - 40% fewer lines of code
- Global-View Resilience (GVR)
 - 85% parallel efficiency on 1K processes with less than 2% code change



Traleika Glacier Project: Impact of advances in runtime systems, compiler optimizations, and new programming models

Technology	Rating	Improvements	Comments
CnC	Promising	9x on Genomics Smith-Waterman	CnC specification and reference are open-sourced by Intel; Rice is now driving changes and enhancements; concern over viability for large-scale applications remains
Habanero	Promising		Habanero C support for OCR works well. The lack of a centralized standard or reference implementation for Habanero hampers ability to support OCR with all Habanero features and versions. Newer HC++ has more features, but has the same underlying challenges.
R-Stream	Good	11x on HPGMG Chebyshev	R-Stream is very effective at automatically generating high- performance OCR code from high-level C loop nests, that exploit the full feature set of OCR and allow users to get the full benefit of the EDT model vs. classical execution models.
OCR (Intel, Rice)	Good	67% weak-scaling efficiency on 8000 nodes	Functionally complete with a precise open specification and reference implementation. Advanced features on resiliency, hints, and related technologies are in progress.
OCR (PNNL)	Promising	323x raytracing over 9600 cores	A version derived from the open reference implementation with a focus on performance first. Source code is not available.
OCR (Vienna)	Promising	62x on Seismic codes	Working on distributed version for larger capabilities; a rapid evaluation framework that has been demonstrated as easy to test new ideas; fully standard compliant; open source.
FSim	Good	Simulating 4,096 cores @ 10MHz ea	Very fast, highly parallel, large-scale architecture simulator works and matches the RTL design model at a functional level. Supports baremetal as well as OCR applications.
Tools	Limited		First-pass tools are working, but are not going to scale as-is to the expected size of future machines without significant additional work. Collaboration with DOE groups working on similar tools is just starting, and will remain in progress. Starting this was delayed until the last year of the XSTG project, due to the evolving OCR design and interfaces.

XPRESS Project: impact of exascale runtime support (HPX)

Comparisons/Results N-body Simulation **1.4x** over MPI (16,384 cores) 1.13x over MPI+OpenMP Mini-Ghost: Boundary Exchange Mini-app (1024 cores) 1.4x over OpenMP Kernels: Stream benchmark Matrix transposition 2.5x over MPI+OpenMP Distributed GPU work 1.5x over native CUDA on 16 GPUs 1.2x over MPI on Cori (128 cores) Lulesh: Shock Hydrodynamics Same as MPI on Cori (4k cores) DSEL and MTL for HPX Same as MPI (256 cores) DOE NNSA DE-NA0002377 (PSAAP2 Same Portable code GPU / CPU **1.34x** for 16 byte puts **1.37**x for 16 byte gets

over MPI-3 one-sided

Photon: Integrated Communication Librar

X-Tune project: impact of Compiler-Directed Auto-tuning

Problem

• Geometric multi-grid (GMG), is one of the most popular methods for solving partial differential equations, but is very difficult to optimize on evolving multi-core architectures.

Solution

- Leverage *communication-avoiding optimizations* which reduce communication overhead.
- Apply CHILL compiler technology, using a set of novel automated code transformations.
- Make the approach portable, via autotuning system that explores tradeoffs between reduced communication and increased computation, as well as tradeoffs in threading strategies.
- Demonstrate optimizations on *mini-GMG*, designed to proxy MGSolve in BoxLib/Chombo codes.

Recent results

- Improved overall multi-grid solve execution by over 3x on NERSC Edison vs. reference version.
 (Basu et al., HIPC 2013, WOSC 2013, WOSC 2014)
- Improved smooth operator at finest level by over 4x.
- CHiLL-generated code outperforms manual tuning.

IMPACT

- Achieves comparable and sometimes better performance than hand-tuned code without sacrificing programmer productivity.
- Demonstrates capability of compiler-directed autotuning, with broad impact on important numerical methods for the DOE Office of Science.

