



X-Stack Portfolio Kickoff Meeting Agenda

September 18-19, Intel Jones Farm Campus, Hillsboro, Oregon

Tuesday, September 18, 2012		
7:00am – 8:00am	Full Breakfast Registration	JF5-1-LC2
8:00am – 8:30am	Opening Remarks (Bill, Sonia)	JF5-1-LC2
8:30am – 9:15am	Traleika Glacier (Intel team)	JF5-1-LC2
9:15am – 10:00am	D-TEC (LLNL team)	JF5-1-LC2
10:00am – 10:30am	Break & posters	JF5-1-LC2
10:30am – 11:15am	DEGAS (LBNL team)	JF5-1-LC2
11:15pm – 12:00pm	XPRESS (Sandia team)	JF5-1-LC2
12:00pm – 1:00pm	Lunch & posters	JF5-1-LC2
1:00pm – 1:30pm	Innovations in Programming Models, Compilers and Runtime Systems for Dynamic Adaptive Event-Driven Execution Models (ETI team)	JF5-1-LC2
1:30pm – 2:00pm	Exploiting Global View for Resilience (GVR) – Andrew Chien	JF5-1-LC2
2:00pm – 2:30pm	Program Correctness, Verification and Testing for Exascale (Corvette)	JF5-1-LC2
2:30pm – 3:30pm	Break & posters	JF5-1-LC2
3:30am – 4:00pm	Autotuning for Exascale: Self-Tuning Software to Manage Heterogeneity in Algrithms, Processors and Memory Systems (Utah team)	JF5-1-LC2
4:00pm – 4:30am	SLEEC: Semantics-rich Libraries for Effective Exa- scale Computation (Purdue team)	JF5-1-LC2
4:30pm – 6:00pm	Discussions & posters	JF5-1-LC2

Wednesday, September 19, 2012

7:00am – 8:00am	Full Breakfast Registration	JF5-1-LC2
8:00am – 8:10am	X-Stack postdocs pool (Sonia)	JF5-1-LC2
8:10am – 9:10am	Co-design centers: explaining the proxy apps (15 minutes for each center)	JF5-1-LC2
9:10am – 10:45am	Parallel session I: X-Stack front end Parallel session II: X-Stack back end	JF5-1-LC2 JF5-120
10:45am – 11:00am	Break	JF5-1-LC2
11:00am – 12:00 noon	Parallel Session I: continue Parallel Session II: continue	JF5-1-LC2 JF5-120
12:00am – 12:30am	Reporting on parallel sessions.	JF5-1-LC2
12:30pm – 1:30pm	Lunch & posters	JF5-1-LC2
1:30pm – 2:30pm	Panel: High Level Representations: Programming Models, DSLs, parallel languages, MPI+X, resilience	JF5-1-LC2
2:30pm – 3:30pm	Panel: Runtime systems (ARTS, OCR, HPX, X10-SEEC, SWARM), resilience	JF5-1-LC2
3:30pm – 4:00pm	Break & posters	JF5-1-LC2
4:00pm – 5:00pm	Panel: Low Level Representations: synthesis, refinements, transformations, resilience	JF5-1-LC2
5:00pm – 5:30pm	Discussions and path forward	JF5-1-LC2